

DESIGN AND ANALYSIS OF A NOVEL ELECTROMAGNETIC BANDGAP STRUCTURE FOR SUPPRESSING SIMULTANEOUS SWITCHING NOISE

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Abstract—An electromagnetic bandgap (EBG) structure is proposed to suppress the simultaneous switching noise (SSN) from 0.45 GHz to 5.3 GHz with an averaged suppression level of -66.4 dB. The design is based on the inductance enhancement by using meander lines to bridge slotted metal patches embedded into the power plane. Numerical simulation and experimental measurement are both used in the study for mutual verification. Compared to the conventional L-bridged EBG structure, the novel design increases the bandwidth by 15% and reduces the lower frequency by 150 MHz. A better omnidirectional SSN suppression is also achieved. For high-speed digital applications, the signal integrity is analyzed and improved.

1. INTRODUCTION

Simultaneous switching noise (SSN) is a bottleneck in the application of modern high-speed digital circuits with fast edge rates and clock frequencies [1]. SSN reduces the noise margin and undermines the performance of analog circuits in the system with mixed signals. The noise from the simultaneous switching of multiple devices induces the voltage fluctuation of the power distribution system, which conversely reduces the signal integrity and causes problems of electromagnetic compatibility [2].

In the last decade, various measures are taken to relieve the SSN, including adding lumped capacitors between the power and ground

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planes, selecting the location of via ports, and employing differential interconnecting circuits, etc [3].

Decoupling capacitors restrict the SSN-induced voltage fluctuation by reducing the high-frequency impedance of the power or ground plane. However, this technique has a frequency limit. Beyond 600 MHz, the application is basically not effective due to the series resonance of the capacitance and the equivalent inductance [4]. Typical SSN has a low-pass spectrum up to 6 GHz, most of which is not covered by the bypassing frequencies of decoupling capacitors.

In recent years, SSN with the spectrum beyond the decoupling range of capacitors is reduced by a novel technique using the high impedance surface. The design is based on the electromagnetic bandgap (EBG) and photonic bandgap (PBG) structures. EBG structures with mushroom-shaped cells and coplanar cells are proposed to inhibit the propagation of cavity-resonance modes and noises [5, 6]. The mushroom-shaped EBG structure uses a layer of periodical metal patches between the power and ground planes. The patches are connected to the ground plane by specially designed connecting vias. Inserted dielectric substrate helps to reduce the size of the structure. The coplanar EBG structure is composed of metal patches embedded in the power plane with bridges between adjacent patches. Compared to the mushroom-shaped EBG structure, the coplanar EBG structure are cost-effective by using standard design and production techniques of printed circuit board (PCB). The power plane with L-bridged coplanar EBG structure provides a 4 GHz bandwidth for SSN suppression from 0.60 GHz to 4.6 GHz [7]. Hybrid EBG structures with lumped chip capacitors and inductors are later developed to reduce the cutoff frequency and expand the suppression bandwidth [8]. Ultra-wideband SSN suppression between 1 GHz and 40 GHz is realized by the combination of low- and high-frequency EBG cells [9]. The coating of a magnetic material over the EBG structure is proved to shift the bandgap toward lower frequencies and improve the SSN propagation loss [10]. The addition of an adjustable air-gap beneath the substrate of the PCB helps to achieve tunable performances of the SSN suppression [11]. In high-speed digital circuits, the meander-line bridging is a novel design of the coplanar EBG structure. Developed from the L-bridged EBG structure, various meander-line EBG structures have promised a feasible and effective technique for SSN suppression [12–14].

In this paper, an MS-EBG structure is introduced into the power plane of the PCB. The structure is featured by the addition of Z-shaped slots etched into square metal patches bridged by meander lines. Numerical simulations and experimental measurement are

used to examine the performance of the novel design. Compared to conventional L-bridged EBG structures, the combination of slots and meander-line bridges of the MS-EBG structure achieves a bandwidth increase of 15% for an improved SSN suppression level of -66.4 dB [7, 12, 13]. The novel design also proves an omnidirectional SSN suppression and minimum influence on the signal integrity.

2. MODEL AND METHODS

2.1. MS-EBG Structure

In high-speed digital circuits, the power and ground planes are embedded into the PCB with multiple circuit layers. To maintain the signal integrity, the power and ground planes are required to be continuous layouts for the direct current. High superficial resistivity on these planes is expected to efficiently filter out the alternating noise [1, 4]. Dimensionally much smaller than the wavelength, the cell of the EBG structure functions as a band-stop filter and is recognized as a parallel resonant circuit quantified by the capacitance C and inductance L . Both parameters are determined by the geometry of the metal patch and the bridge: the gap between neighboring patches provides C , whereas L is introduced by the bridge. The central frequency of the suppression band is quasi-quantitatively described by $(2\pi\sqrt{LC})^{-1}$. The relative bandwidth is in direct proportion to

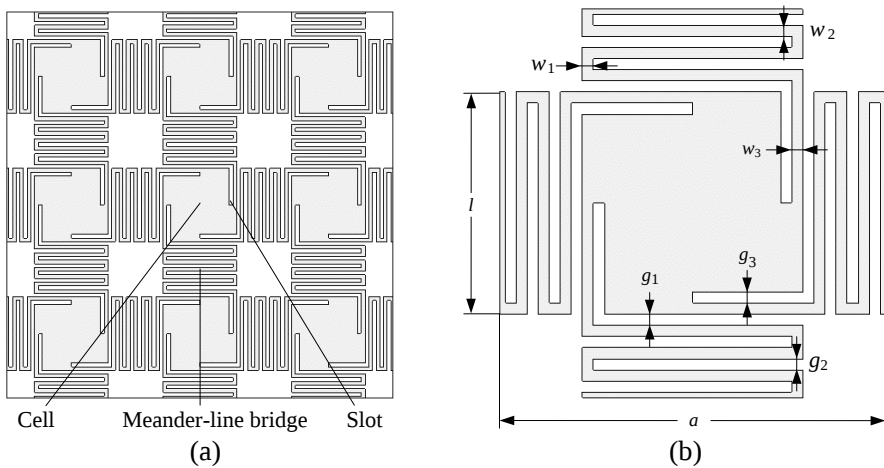


Figure 1. (a) MS-EBG structure with meander-line bridges and slots. (b) Geometry parameters of a cell.

$\sqrt{L/C}$. Based on these principles, an EBG structure with better SSN suppression is possible if we geometrically increase the inductance and decrease the capacitance to reduce the central frequency and expand the relative bandwidth.

The EBG structure of this study is characterized by the inductance enhancement with bridges of meander lines and slots etched into the metal patch. The configuration is named meander-line-slot EBG (MS-EBG) structure, shown in Figure 1(a). The EBG structure consists of an array of 3×3 cells. The geometry of a cell is parameterized, as shown in Figure 1(b), where the length of the cell $a = 30$ mm; the widths and length of meander lines are $w_1 = w_2 = w_3 = 0.2$ mm and $l = 27$ mm; the gaps are $g_1 = g_2 = g_3 = 0.2$ mm.

2.2. Numerical Simulation

A finite element software (Ansoft HFSS) is used in the numerical study of the transmission properties to describe the insertion loss and quantify the SSN suppression of the MS-EBG structure. The MS-EBG structure is embedded into the power plane of a double-layer PCB. The ground plane is a plain sheet. Identical simulation parameters are selected for comparison to previous results from other typical designs. The FR4 substrate has a relative permittivity of 4.4 and loss tangent of 0.02 over the frequency range of interest. The metal layers are silver-gilded copper and parameterized as the perfect electric conductor.

The HFSS model of the PCB with the MS-EBG power plane is shown in Figure 2(a). The size of the PCB is $90 \text{ mm} \times 90 \text{ mm} \times 0.4 \text{ mm}$. Three ports are added into the model. The input is port 1 at

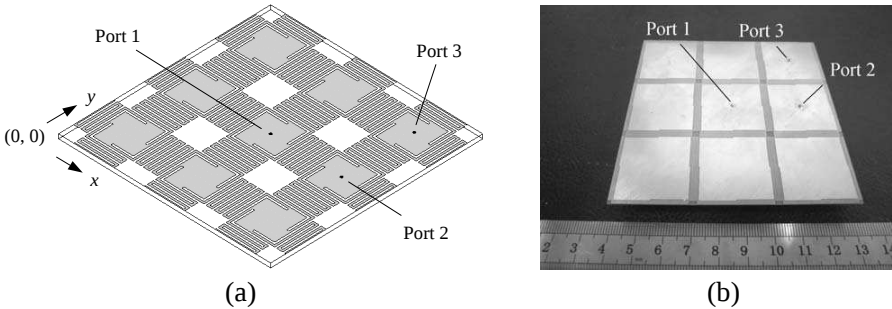


Figure 2. (a) Model of the PCB with the MS-EBG power plane for numerical simulation. (b) Photograph of the PCB with the MS-EBG power plane for experimental measurement.

$(x, y) = (45 \text{ mm}, 45 \text{ mm})$. Two outputs are port 2 at $(75 \text{ mm}, 45 \text{ mm})$ and port 3 at $(75 \text{ mm}, 75 \text{ mm})$. A lumped source is added to the input port and simulates the excitation from a standard 50Ω cable. The transmission properties from the input port to the output ports are calculated.

2.3. Experimental Measurement

To validate the accuracy of the HFSS simulation, we use a vector network analyzer (Agilent N5230a) to measure the transmission properties of the MS-EBG power plane. Due to the available material, the thickness of the PCB is 0.5 mm instead of the previous 0.4 mm. Other geometry dimensions are unchanged. An HFSS simulation of the 0.5 mm PCB is performed to provide numerical data for comparison.

The photograph of the fabricated PCB with the MS-EBG power plane is shown in Figure 2(b). SMA connectors with 50Ω power matching are added to the input port and output ports. The transmission loss of the connectors and cables are measured for compensation.

3. VALIDATION

The comparison between the experimental measurement and the numerical simulation is shown in Figure 3. For the MS-EBG power plane on the 0.5 mm PCB, a good agreement is found for transmission properties quantified by S_{21} and S_{31} across the frequency range from 0.45 GHz to 5.3 GHz. The bandwidth parameters are also very close between the experimental and numerical data, as listed in Table 1,

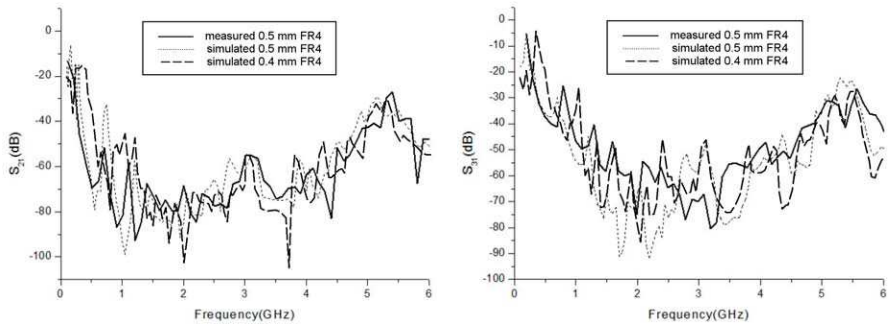


Figure 3. Comparison of S_{21} and S_{31} between the measured and simulated MS-EBG power planes.

Table 1. Comparison of the bandwidth parameters between the measured and simulated MS-EBG power planes.

Results	S_{21}		S_{31}	
	−30 dB bandgap (GHz)	Relative suppression bandwidth (%)	−30 dB bandgap (GHz)	Relative suppression bandwidth (%)
Measured	4.96	182	4.78	150
Simulated	4.78	176	4.85	175

where the suppression bandwidth is measured by −30 dB bandgap. Simulated S_{21} and S_{31} results from the MS-EBG power plane on the 0.4 mm PCB are also displayed in Figure 3. It is indicated that the change of the PCB thickness between 0.4 mm and 0.5 mm induces little variance in the transmission properties with no significant influence on the SSN suppression. The validation guarantees the accuracy of the following numerical results of the MS-EBG power plane on the 0.4 mm PCB.

4. RESULTS

4.1. SSN Suppression

For a comparative study on the performance of the MS-EBG power plane, we also consider a reference power plane and an L-bridged EBG power plane. The reference power plane is a plain layout with the same PCB dimensions, as well as the input and output ports. The L-bridged EBG power plane is selected from that in [7] for structural similarity. The suppression bandwidth is still quantified by −30 dB bandgap. Because of the geometry difference, we did not use the meander-line EBG structure proposed in [12], where three cells exist between the input and output ports and the bandwidth is measured by −28 dB. The transmission properties are compared in Figure 4. The SSN suppressions of the MS-EBG and L-bridged EBG power planes are obviously effective. Measured by S_{21} , the 0.60 GHz–4.6 GHz bandgap of the L-bridged EBG power plane is extended to 0.45 GHz–5.3 GHz by the MS-EBG power plane. The lower frequency is reduced by 150 MHz. Similar result of the bandgap expansion is found for S_{31} . The reduction of the lower frequency is expected and in accordance with the theoretical analysis [15]. The lower frequency is inversely proportional to the inductance introduced by the connection between adjacent cells. The addition of the slot etched into the cell helps to increase the

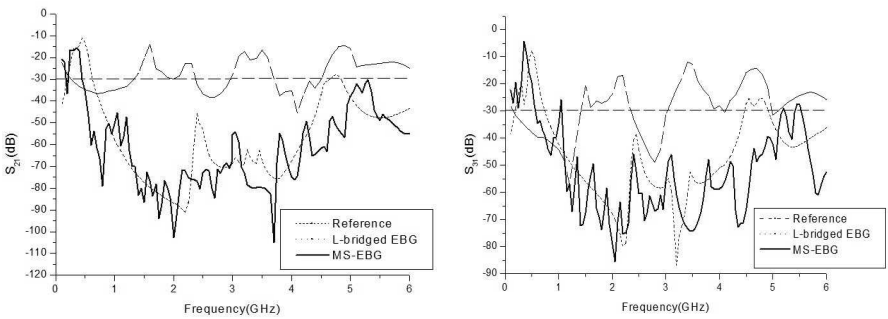


Figure 4. Comparison of S_{21} and S_{31} among the reference, L-bridged EBG, and MS-EBG power planes.

Table 2. Comparison of the SSN suppression between the L-bridged EBG and MS-EBG power planes.

Power plane	S_{21}		S_{31}	
	−30 dB bandgap (GHz)	Averaged suppression level (dB)	−30 dB bandgap (GHz)	Averaged suppression level (dB)
L-bridged EBG	4.00	−62.2	3.65	− 52.9
MS-EBG	4.85	−66.4	4.65	− 59.3

overall length of the connection and the inductance, thus shifting the lower frequency downward. A quantified comparison between the two EBG power planes is listed in Table 2. Compared to the L-bridged EBG power plane, the MS-EBG power plane expands the relative suppression bandwidth from 154% to 169% (15% absolute expansion) as measured by S_{21} . For S_{31} , the relative suppression bandwidth is expanded from 135% to 163% (28% absolute expansion). For the MS-EBG power plane, the averaged suppression levels are lower than those of the L-bridged EBG power plane. The suppression bandwidths and averaged suppression levels are similar between S_{21} and S_{31} , indicating a better omnidirectional SSN suppression of the novel design.

4.2. Signal Integrity

The wideband SSN suppression of the MS-EBG structure has cost the continuity of the power plane, thus potentially degrading the integrity of the signal in high-speed digital circuits. To evaluate the effect of the structure on the signal integrity, we designed a four-layer PCB, as

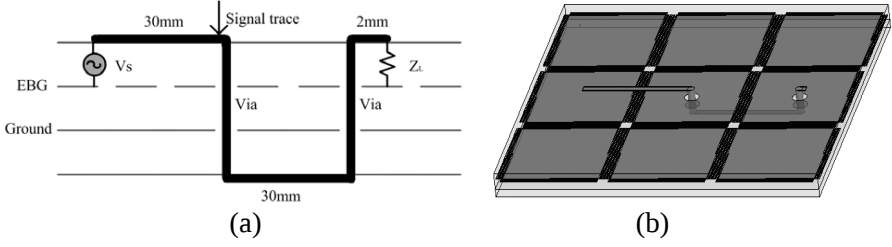


Figure 5. (a) Side view of the four-layer PCB with the signal trace. (b) Configuration of the transmission line.

shown in Figures 5(a) and 5(b). The two inner layers are the power and ground planes, whereas the top and bottom layers are signal planes. A 1-mm-wide transmission line with $50\ \Omega$ characteristic impedance guides the signal from the top plane to the bottom plane and returns to the top through two vias. The signal trace is 64 mm. Ansoft Designer is applied in the analysis. The pseudo-random binary sequence is launched at the source V_s . The sequence is coded at 2.5 GHz. Logic 0 and logic 1 are set at levels of 0 mV and 500 mV, respectively, with 120 ps normal rise/fall time. At the $50\ \Omega$ load Z_L , the signal integrity is observed as the eye diagram characterized and quantified by the maximum eye open (MEO) and the maximum eye width (MEW).

Figures 6(a) and 6(b) compare the eye patterns between the reference power plane and the MS-EBG power plane. It is read that the MEO and MEW are 415 mV and 380 ps for the reference power plane. For the MS-EBG power plane, the MEO and MEW are reduced to 252 mV and 361 ps, respectively. The signal integrity is sensitively degraded by the MS-EBG structure. The use of differential pair of transmission lines effectively improves the signal integrity with the MS-EBG application. In Figure 6(c), the single transmission line is replaced by a differential pair of transmission lines. In the geometry, each line is 0.5 mm wide with the characteristic impedance of $100\ \Omega$. The lines are separated by 0.25 mm. The result of the eye pattern is shown in Figure 6(d). The openness of the eye pattern is improved significantly. The MEO and MEW are 390 mV and 373 ps. Compared to the reference power plane, the reductions of MEO and MEW are 6.0% and 1.8%, respectively, indicating a minimum influence on the signal integrity of the MS-EBG power plane with the use of differential pair of transmission lines.

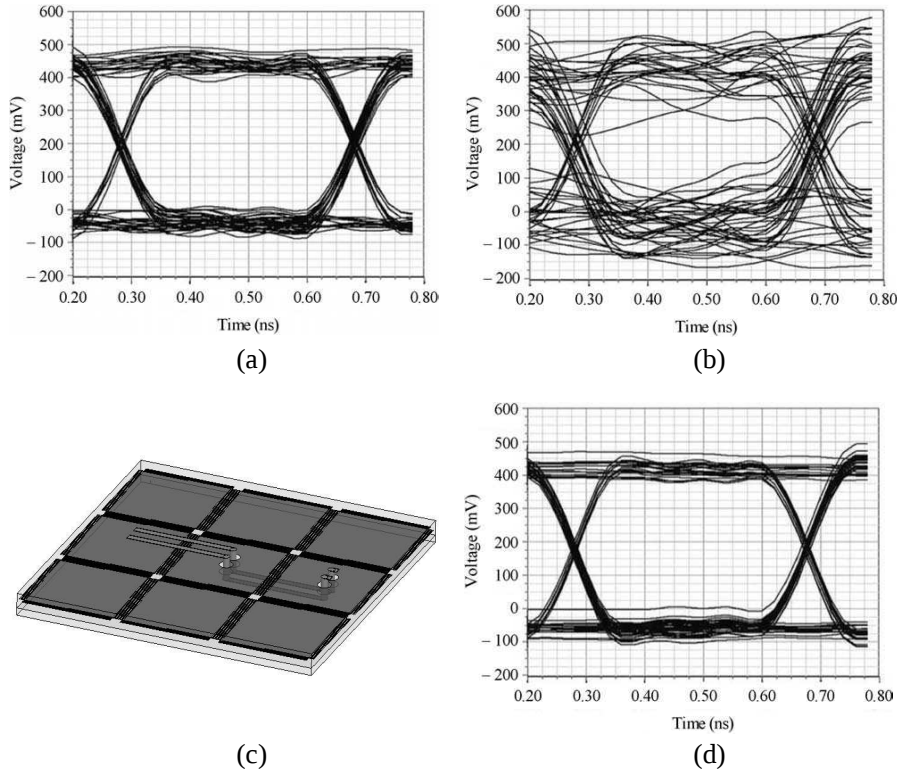


Figure 6. (a) Eye diagram of the reference power plane. (b) Eye diagram of the MS-EBG power plane. (c) Configuration of the differential pair of transmission lines. (d) Eye diagram with the use of differential pair of transmission lines on the MS-EBG power plane.

5. CONCLUSION

The proposed MS-EBG structure enhances the inductance for wideband SSN suppression by using the combination of meander-line bridge connections and slots etched in metal patches. Supported by experimental measurement, the numerical simulation of the power plane with the novel design shows an effective SSN suppression from 0.45 GHz to 5.3 GHz. The suppression bandwidth is expanded by 15% and the lower frequency is reduced by 150 MHz, as compared to the L-bridged EBG power plane of the same geometry. The MS-EBG power plane has a far better omnidirectional SSN suppression. The signal integrity is improved to minimum MEO and MEW variances with the use of differential pair of transmission lines.

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